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7590 05/03/2006

McDermott Will & Emery LLP
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"CUSTOMER NO.: 20277"

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/714,393	11/17/2003	Hideyuki Noda	57454-990	2061

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A LOGIC CIRCUIT AND A DYNAMIC RANDOM ACCESS MEMORY (DRAM) MERGED ON THE SAME CHIP (As Amended)

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/03/2006
EXAMINER		ART UNIT		CLASS-SUBCLASS	
TRAN, ANDREW Q		2824		365-227000	

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 McDERMOTT WILL &
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

RENESAS TECHNOLOGY CORP.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

- Issue Fee
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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Gene Z. Rubinson

Typed or printed name

Gene Z. Rubinson

07/24/2006 18EYENP 00000107 500417 10714393
Date 01 FC:1501 1400.00 DA
62 FEE1504 3003.00 DA
03 FC:8001 6.00 DA

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